

REMARKS

The Examiner's Advisory Action of January 22, 2004 has been received and its contents reviewed. Applicants would like to thank the Examiner for the consideration given to the above-identified application.

By the above actions, claims 1, 6, 11, 14, 17 and 22 have been amended. Accordingly, claims 1-32 are pending for consideration, of which claims 1, 6, 11, 14, 17 and 22 are independent. In view of these actions and the following remarks, reconsideration of this application is now requested.

In the Advisory Action, the examiner contends that Sawada teaches a control circuit in the form of a display mode dependence controller (17) that is connected to the digital image processor 3 and the scanning control circuit 22 (Figure 1, features 3 and 17). The Examiner further contends that, while Sawada does not show the controller 17 feeding directly to display unit 4, the connection of the controller 17 to the scanning control circuit 22 reads directly on Applicants' circuit connectivity as shown in Figure 1 (Compare Figure 1 of Applicants and Sawada). Still further, the Examiner asserts that, while Sawada does not teach explicitly a digital video signal dividing circuit, Kuwabara teaches this concept by teaching a driving method for an active matrix type image display having a plurality of video signal lines wherein a scheme is designed to achieve a digital video dividing circuit of an original video signal (column 12, lines 10-27, Figure 7).

In response to the Examiner's reasoning regarding Sawada, Applicants have amended the claims, as shown above, to further distinguish the claimed structure of the present invention from the structure of Sawada. Specifically, Applicants have amended claims 1, 6, 11, 14, 17 and 22 to recite that the control circuit directly feeds pulses to an image signal processing circuit, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit.

With respect to the Examiner's assertion that Kuwabara teaches a plurality of video signal lines wherein a scheme is designed to achieve a digital video dividing circuit, Applicants respectfully assert that the digital video signal dividing circuit of Kuwabara appears to be formed within the source driver circuit (Fig. 7). In the present invention, the digital video signal dividing circuit and the source driver circuit appear to be formed individually. In order to clarify this difference, Applicants have amended claims 1, 6, 11, 14,

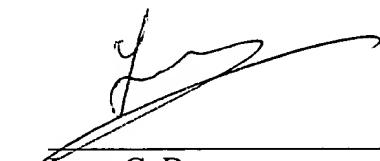
17 and 22 so as to recite that the digital video signal dividing circuit feeds the source driver circuit with a digital video signal.

The requirements for establishing a *prima facie* case of obviousness, as detailed in MPEP § 2143 - 2143.03 (pages 2100-122 - 2100-136), are: first, there must be some suggestion or motivation, either in the reference themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference to combine the teachings; second, there must be a reasonable expectation of success; and, finally, the prior art reference (or references when combined) must teach or suggest all of the claim limitations. As Suwada and Kuwabara fail to teach, disclose, or suggest the control circuit directly feeding pulses to an image signal processing circuit, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit and the digital video signal dividing circuit feeding the source driver circuit with a digital video signal as recited in the amended claims, a *prima facie* case of obviousness has not been established.

In view of the amendments and arguments set forth above, Applicants respectfully request reconsideration and withdrawal of all the pending rejections.

While the present application is now believed to be in condition for allowance, should the Examiner find some issue to remain unresolved, or should any new issues arise, which could be eliminated through discussions with Applicants' representative, then the Examiner is invited to contact the undersigned by telephone in order that the further prosecution of this application can thereby be expedited.

Respectfully submitted,



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